

XA-9604B
PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Hiroyuki ENOMOTO et al.

Appln. No.

Filed: HERewith

For: THE METHOD OF PRODUCING A SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE AND THE SEMICONDUCTOR INTEGRATED CIRCUIT
DEVICE

* * *

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Prior to examination, please amend the above-identified
patent application as indicated below.

IN THE SPECIFICATION:

Page 1:

after the title, insert the following section: